

Arrangement of Configurable Logic Blocks

ABSTRACT OF THE DISCLOSURE

A logic circuit realizes an "if then else" branch based upon information carried at an input node and information stored in a register. The logic circuit includes at least one register and a corresponding comparator coupled to the register and an input data node. A multiplexer is coupled to the register. A control block is coupled to the multiplexer, the comparator, the input node and an input control node.